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J658 U.S. PRO
09/661912

105	Subclass	ISSUE CLASSIFICATION
710	Class	

PATENT NUMBER

U.S. UTILITY Patent Application

KW	O.I.P.E.	PATENT DATE
SCANNED	BKQ	QA

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/661912	D	711	210	105 21	2185

APPLICANTS:
Saeed Afzimi

TITLE:
High latency interface between hardware components

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
710	105	710	105	106	205	307	
INTERNATIONAL CLASSIFICATION			33				
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket							

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/>	5	5	2	75	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<i>Juthay</i> 7-9-83 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
ISSUE BATCH NUMBER					

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